**USN** 

## Sixth Semester B.E. Degree Examination, Dec.2013/Jan.2014

Software Testing Time: 3 hrs. Max. Marks: 100 Note: Answer FIVE full questions, selecting at least TWO questions from each part. PART - A What is software testing? Why it is so important in SDLC? Explain the triangle problem statement along with flow chart for traditional implementation. Explain the IEEE error and fault taxonomy and IEEE standard anomaly process. (08 Marks) ¢. 2 Justify the usage of boundary value analysis with function of two variables and highlight the limitations of boundary value analysis. (05 Marks) b. Explain weak normal and strong robust equivalence class testing with next date problem as an example. (05 Marks) Discuss the usage of decision table method to device test case with example of commission problem and triangle problem. c. problem and triangle problem. (10 Marks) Define DD-path. Draw DD graph for triangle problem 3 (04 Marks) Justify strongly connected couph is the number of linearly independent circuits in the graph using cyclomatic complexity metric. (04 Marks) Define predicate node, du-paths and de-paths. Give du-paths for stocks, locks, total locks, sales and commission for commission sale problem. (12 Marks) Explain the simple ATM application with the help of, (i) Level 1 data flow diagram. (ii) Upper level finite state machine (10 Marks) Distinguish between top-down integration and lottom-up integration. (04 Marks) b. Explain call graph-based integration with the help of, (ii) Neighborhood integration. (i) Pair-wise integration (06 Marks) PART - B Define the below terms: 5 a. (iii) Data (iv) Actions (v) Ports (10 Marks) (i) Threads (ii) MM-path Explain single-processor static interaction and single-processor dynamic interaction. b. (10 Marks) (08 Marks) Explain verification trade-off dimensions. 6 a. Bric discuss the dependability properties in process framework. (08 Marks) b. (04 Marks) organizational factors are needed in process framework. ¢. Define below terms with respect to fault based-testing: Original program ii) Program location. (08 Marks) iii) Alternate expression iv) Alternate program. Explain mutation analysis software fault based testing. (04 Marks) b. List the Fault-based adequacy criterias. (03 Marks) d. Explain hardware fault-based testing. (05 Marks) 8 Write a short note on:

- a. Quality and process.
- Test planning. b.
- Risk planning. C.
- d. Organizing documents.
- Test design specification document. (20 Marks)